

Patent 7,421,592

PATENT

**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Andrew Kadatch et al.

Examiner: TRAN, VINCENT HUY

Patent No.: 7,421,592

Group Art Unit: 2115

Issue Date: September 2, 2008

Confirmation No.: 7435

Customer No.: **22971**

Docket No: 305998.01

Title: HIGH PERFORMANCE COUNTER FOR REALISTIC MEASUREMENT OF  
COMPUTER SYSTEM LOAD

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**REQUEST FOR CERTIFICATION OF CORRECTION**

Attn: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

**As the error is that of the Patent Office, it is believed that no fee is due.**

Respectfully submitted,

**Customer Number: 22971**

Date 1/18/2011



Christine Hartness

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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We are transmitting herewith the attached:

☒ Request for Certificate of Correction (1 pg.).

☒ Certificate of Correction Form - PTO-1050 (1 pg.)

**Customer Number: 22971**

*Christine Hartness*

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Christine Hartness

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO: 7,421,592

DATED: September 2, 2008

INVENTOR(S): Andrew Kadatch et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 16, line 34, in Claim 11, after "thread" insert - - , - - .

MAILING ADDRESS OF SENDER:

CPA Global  
P.O. Box 52050  
Minneapolis, MN 55402

Any Docket No. 365998.01

PATENT NO. 7,421,592

No. of additional copies



Page 3 of 3